

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Inventors: Katsuhiko TSUURA Prior Art Unit: 2811
Application No.: Divisional Application of Examiner: P. Dang
Serial No. 10/369,548,
filed February 23, 2003
Filed: September 30, 2003
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND
METHOD OF PRODUCING THE SAME

PRELIMINARY AMENDMENT

Assistant Commissioner of Patents
Washington, DC 20231

Sir:

After grant of a filing date, please amend the above-captioned
application as follows: